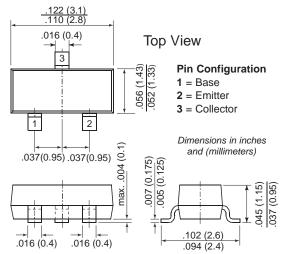


#### **Small Signal Transistors (NPN)**



#### TO-236AB (SOT-23)



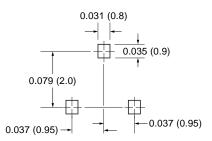
#### **Mechanical Data**

**Case:** SOT-23 Plastic Package **Weight:** approx. 0.008g

Packaging Codes/Options:

E8/10K per 13" reel (8mm tape), 30K/box E9/3K per 7" reel (8mm tape), 30K/box

#### **Mounting Pad Layout**



Туре	Marking
BC846A	1A
B	1B
BC847A	1E
В	1F
С	1G

Туре	Marking
BC848A	1J
В	1K
C	1L
BC849B	2B
С	2C

#### **Features**

- NPN Silicon Epitaxial Planar Transistors for switching and AF amplifier applications.
- Especially suited for automatic insertion in thick and thin-film circuits.
- These transistors are subdivided into three groups (A, B, and C) according to their current gain. The type BC846 is available in groups A and B, however, the types BC847 and BC848 can be supplied in all three groups. The BC849 is a low noise type available in groups B and C. As complementary types, the PNP transistors BC856...BC859 are recommended.

#### Maximum Ratings and Thermal Characteristics (TA = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit	
Collector-Base Voltage	BC846 BC847 BC848, BC849	Vсво	80 50 30	V	
Collector-Emitter Voltage  BC846 BC847 BC848, BC849		Vces	80 50 30	V	
Collector-Emitter Voltage  BC846 BC847 BC848, BC849		VCEO	65 45 30	V	
Emitter-Base Voltage	BC846, BC847 BC848, BC849	V <sub>EBO</sub>	6 5	V	
Collector Current		Ic	100	mA	
Peak Collector Current		Ісм	200	mA	
Peak Base Current		Івм	200	mA	
Peak Emitter Current		-I <sub>EM</sub>	200	mA	
Power Dissipation at Tsb = 50°C		Ptot	310 <sup>(1)</sup>	mW	
Thermal Resistance Junction to Ambiant Air		RθJA	450 <sup>(1)</sup>	°C/W	
Thermal Resistance Junction to Substrate Backside		R <sub>0</sub> SB	320 <sup>(1)</sup>	°C/W	
Junction Temperature		Tj	150	°C	
Storage Temperature Range		Ts	-65 to +150	°C	



### **Small Signal Transistors (NPN)**

#### Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Small Signal Current Gain Current Gain Group A B C	h <sub>fe</sub>	VCE = 5V, IC = 2mA f = 1kHz	_ _ _	220 330 600	_ _ _	_ _ _
Input Impedance  Current Gain Group A  B  C	hie	VCE = 5V, IC = 2mA f = 1kHz	1.6 3.2 6.0	2.7 4.5 8.7	4.5 8.5 15.0	kΩ
Output Admittance  Current Gain Group A  B  C	h <sub>oe</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 2mA f = 1kHz		18 30 60	30 60 110	μS
Reverse Voltage Transfer Ratio Current Gain Group A B C	h <sub>re</sub>	VCE = 5 V, IC = 2mA f = 1kHz		1.5 · 10 <sup>-4</sup> 2 · 10 <sup>-4</sup> 3 · 10 <sup>-4</sup>	_ _ _	_ _ _
DC Current Gain  Current Gain Group A  B  C	hFE	VCE = 5V, IC = 10μA	_ _ _	90 150 270	_ _ _	_ _ _
Current Gain Group A B C	hFE	VCE = 5V, IC = 2mA	110 200 420	180 290 520	220 450 800	_ _ _
Collector Saturation Voltage	VCEsat	I <sub>C</sub> = 10mA, I <sub>B</sub> = 0.5mA I <sub>C</sub> = 100mA, I <sub>B</sub> = 5mA	_	90 200	250 600	mV
Base Saturation Voltage	V <sub>BEsat</sub>	$I_{C} = 10$ mA, $I_{B} = 0.5$ mA $I_{C} = 100$ mA, $I_{B} = 5$ mA		700 900	_	mV
Base-Emitter Voltage	V <sub>BEon</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 2mA V <sub>CE</sub> = 5V, I <sub>C</sub> = 10mA	580 —	660 —	700 770	mV
Collector-Base Cutoff Current	Ісво	V <sub>CB</sub> = 30V V <sub>CB</sub> = 30V, T <sub>J</sub> = 150°C	_	_	15 5	nA μA
Gain-Bandwidth Product	f⊤	V <sub>CE</sub> = 5V, I <sub>C</sub> = 10mA f = 100MHz	_	300	_	MHz
Collector-Base Capacitance	Ссво	V <sub>CB</sub> = 10V, f = 1MHz	_	3.5	6	pF
Emitter-Base Capacitance	Сево	VEB = 0.5V, f = 1MHz	_	9	_	pF
BC846, BC847, BC848 BC849 Noise Figure	F	VCE = 5V, IC = 200 $\mu$ A RG=2kΩ, f=1kHz, $\Delta$ f=200 Hz	_	2 1.2	10 4	dB dB
BC849		$V_{CE} = 5V$ , $I_{C} = 200$ μA $R_{G} = 2k\Omega$ , $f = 3015000$ Hz	_	1.4	4	dB

Note: (1) Device on fiberglass substrate, see layout on next page

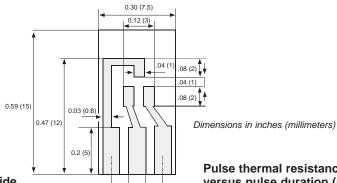


### **Small Signal Transistors (NPN)**

#### Layout for R<sub>⊕</sub>J<sub>A</sub> test

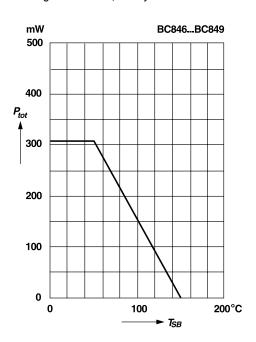
Thickness:

Fiberglass 0.059 in. (1.5 mm) Copper leads 0.012 in. (0.3 mm)

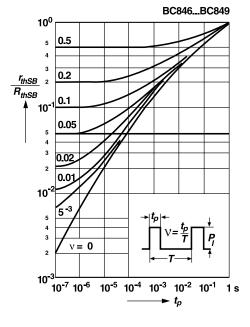


0.06 (1.5) 0.20 (5.1)

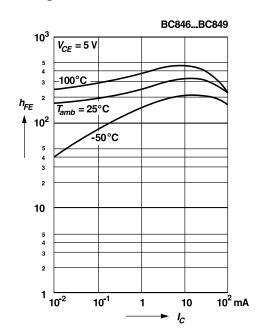
Admissible power dissipation versus temperature of substrate backside Device on fiblerglass substrate, see layout



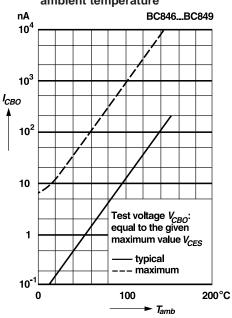
Pulse thermal resistance versus pulse duration (normalized) Device on fiblerglass substrate, see layout



DC current gain versus collector current



Collector-Base cutoff current versus ambient temperature

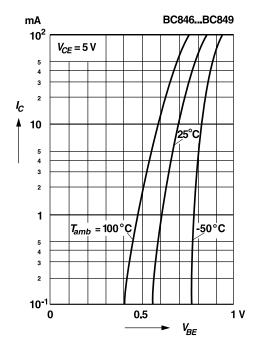




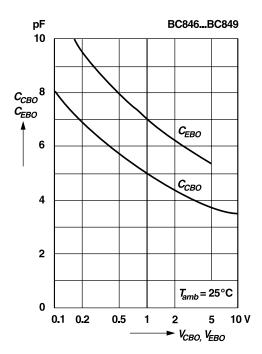
### **Small Signal Transistors (NPN)**

# Ratings and Characteristic Curves (TA = 25°C unless otherwise noted)

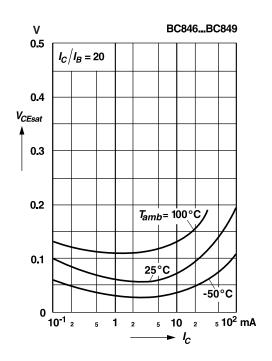
Collector current versus base-emitter voltage



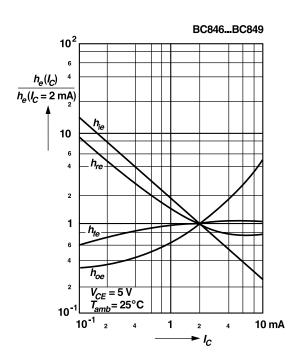
Collector base capacitance, Emitter base capacitance versus reverse bias voltage



Collector saturation voltage versus collector current



Relative h-parameters versus collector current

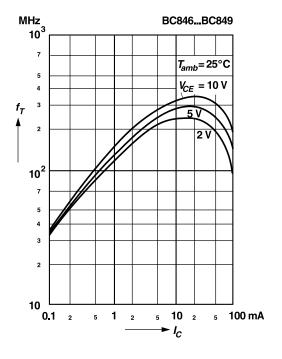




### **Small Signal Transistors (NPN)**

#### **Ratings and** Characteristic Curves (TA = 25°C unless otherwise noted)

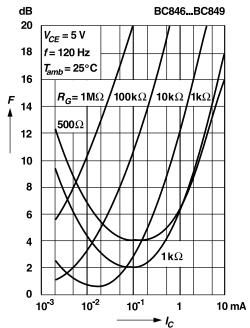
Gain-bandwidth product versus collector current



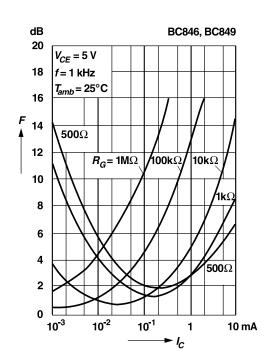
dΒ

versus collector current

Noise figure



Noise figure versus collector current



Noise figure versus collector emitter voltage

