FIDI USB-RS485-WE-1800-BT 4 cores P/N: 200579

New original chip FTDI FT232RQ

Support Win XP 7 8 10 Android MAC Android

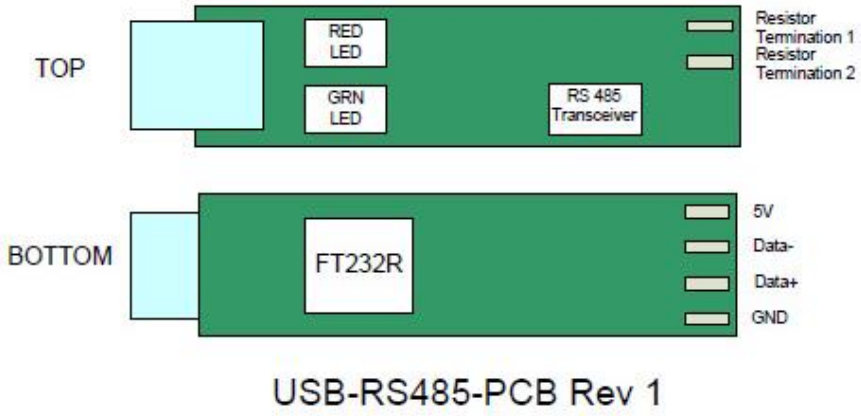
The standard pull-down resistance is 10K, without terminal resistance, and 120r is an optional

The FTDI standard wiring mode shown in the figure below.

1.8m transparent molding chip model visible

Fully compatible with the original product line: ftdi usb-rs485-we-1800-bt

4 USB-RS485-PCB Connector Pin Out and Mechanical details



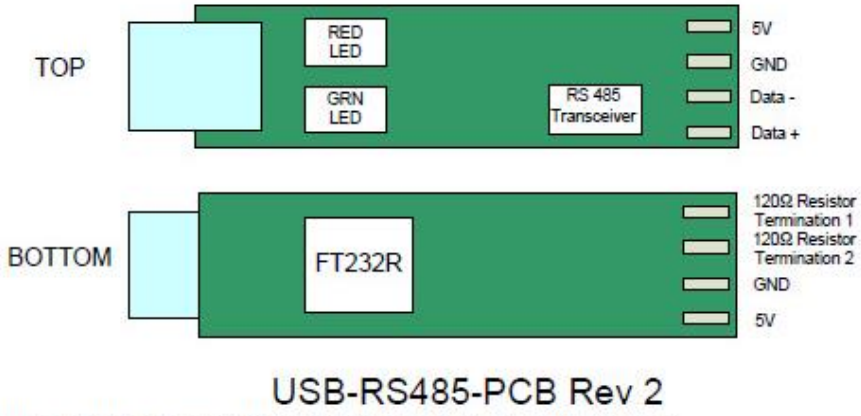
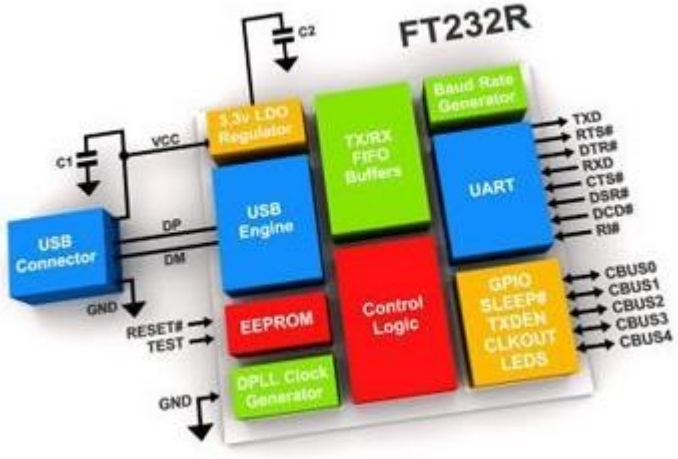
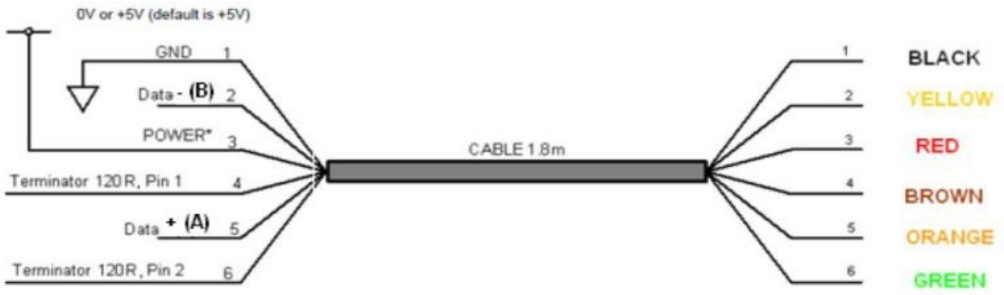


Figure 4.1 USB-RS485-PCB Pin Out (Top is TOP View, Bottom is BOTTOM View)





FT232RL features:

Single chip USB to asynchronous serial data transfer interface.

Entire USB protocol handled on the chip - No USB-specific firmware programming required.

UART interface support for 7 or 8 data bits, 1 or 2 stop bits and odd / even / mark / space / no parity.

Fully assisted hardware or X-On / X-Off software handshaking.

Data transfer rates from 300 baud to 3 Megabaud (RS422 / RS485 and at TTL levels) and 300 baud to 1 Megabaud (RS232).

In-built support for event characters and line break condition.

New USB FTDIChip-ID? feature.

New configurable CBUS I/O pins.

Auto transmit buffer control for RS485 applications.

Transmit and receive LED drive signals.

New 48MHz, 24MHz,12MHz, and 6MHz clock output signal Options for driving external MCU or FPGA.

FIFO receive and transmit buffers for high data throughput.

256 Byte receive buffer and 128 Byte transmit buffer utilising buffer smoothing technology to allow for high data throughput.

Adjustable receive buffer timeout.

Synchronous and asynchronous bit bang mode interface options with RD# and WR# strobes.

New CBUS bit bang mode option.

Integrated 1024 bit internal EEPROM for I/O configuration and storing USB VID, PID, serial number and product description strings.

Device supplied preprogrammed with unique USB serial number.

Support for USB suspend / resume.

Support for bus powered, self powered, and high-power bus powered USB configurations.

Integrated 3.3V level converter for USB I/O .

Integrated level converter on UART and CBUS for interfacing to 5V - 1.8V Logic.

True 5V / 3.3V / 2.8V / 1.8V CMOS drive output and TTL input.

High I/O pin output drive option.

Integrated USB resistors.

Integrated power-on-reset circuit.

Fully integrated clock - no external crystal, oscillator, or resonator required.

Fully integrated AVCC supply filtering - No separate AVCC pin and no external R-C filter required.

UART signal inversion option.

USB bulk transfer mode.

3.3V to 5.25V Single Supply Operation.

Low operating and USB suspend current.

Low USB bandwidth consumption.

UHCI / OHCI / EHCI host controller compatible.

USB 2.0 Full Speed compatible. -40°C to 85°C extended operating temperature range.