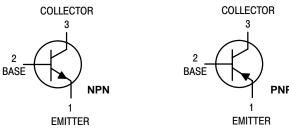


# **Amplifier Transistors**



#### **MAXIMUM RATINGS**

Rating	Symbol	MPS650 MPS750	MPS651 MPS751	Unit
Collector–Emitter Voltage	VCE	40	60	Vdc
Collector-Base Voltage	V <sub>CB</sub>	60	80	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	5.0		Vdc
Collector Current — Continuous	IC	2.0		Adc
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	625 5.0		mW mW/°C
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	1.5 12		Watt mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150		°C

# NPN MPS650 MPS651\* PNP MPS750 MPS751\*

Voltage and current are negative for PNP transistors

\*ON Semiconductor Preferred Devices



#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	83.3	°C/W

### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Charact	eristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage(1) (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 0)	MPS650, MPS750 MPS651, MPS751	V(BR)CEO	40 60	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 100 μAdc, I <sub>E</sub> = 0 )	MPS650, MPS750 MPS651, MPS751	V(BR)CBO	60 80		Vdc
Emitter–Base Breakdown Voltage (I <sub>C</sub> = 0, I <sub>E</sub> = 10 μAdc)		V(BR)EBO	5.0	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 60 Vdc, I <sub>E</sub> = 0) (V <sub>CB</sub> = 80 Vdc, I <sub>E</sub> = 0)	MPS650, MPS750 MPS651, MPS751	ICBO	_	0.1 0.1	μAdc
Emitter Cutoff Current (VEB = 4.0 V, IC = 0)		IEBO	_	0.1	μAdc

<sup>1.</sup> Pulse Test: Pulse Width  $\leq 300~\mu s,$  Duty Cycle = 2.0%.

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

## **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS(1)				
DC Current Gain (I <sub>C</sub> = 50 mA, V <sub>CE</sub> = 2.0 V) (I <sub>C</sub> = 500 mA, V <sub>CE</sub> = 2.0 V) (I <sub>C</sub> = 1.0 A, V <sub>CE</sub> = 2.0 V) (I <sub>C</sub> = 2.0 A, V <sub>CE</sub> = 2.0 V)	hFE	75 75 75 40	_ _ _ _	_
Collector–Emitter Saturation Voltage $(I_C = 2.0 \text{ A}, I_B = 200 \text{ mA})$ $(I_C = 1.0 \text{ A}, I_B = 100 \text{ mA})$	VCE(sat)	_	0.5 0.3	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = 1.0 A, V <sub>CE</sub> = 2.0 V)	V <sub>BE(on)</sub>	_	1.0	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 100 mA)	V <sub>BE(sat)</sub>	_	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS				•
Current-Gain — Bandwidth Product(2) (I <sub>C</sub> = 50 mAdc, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)	fT	75	_	MHz

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle = 2.0%. 2. f<sub>T</sub> is defined as the frequency at which |hfe| extrapolates to unity.

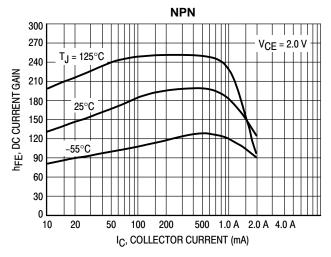


Figure 1. MPS650, MPS651 Typical DC Current Gain

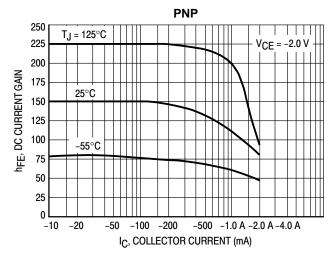


Figure 2. MPS750, MPS751 Typical DC Current Gain

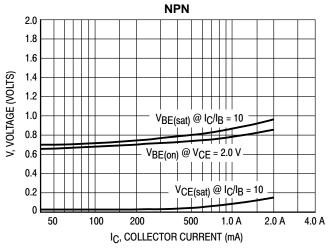


Figure 3. MPS650, MPS651 On Voltages

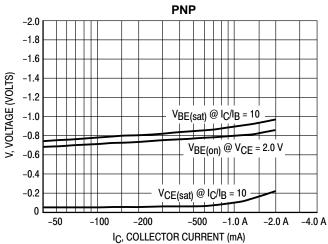


Figure 4. MPS750, MPS751 On Voltages

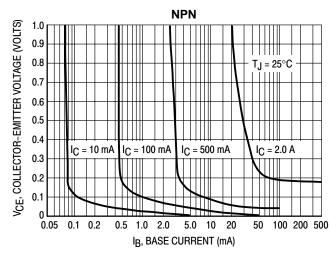


Figure 5. MPS650, MPS651 Collector Saturation Region

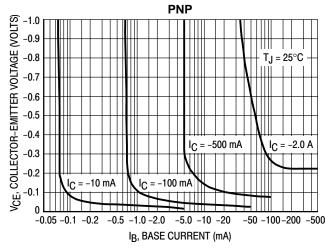


Figure 6. MPS750, MPS751 Collector Saturation Region

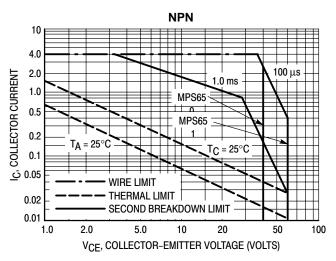


Figure 7. MPS650, MPS651 SOA, Safe Operating Area

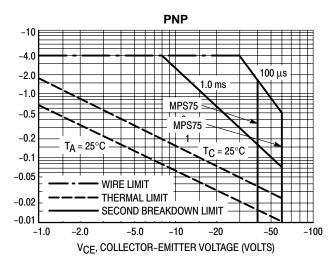
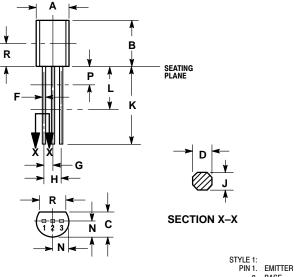


Figure 8. MPS750, MPS751 SOA, Safe Operating Area

#### **PACKAGE DIMENSIONS**

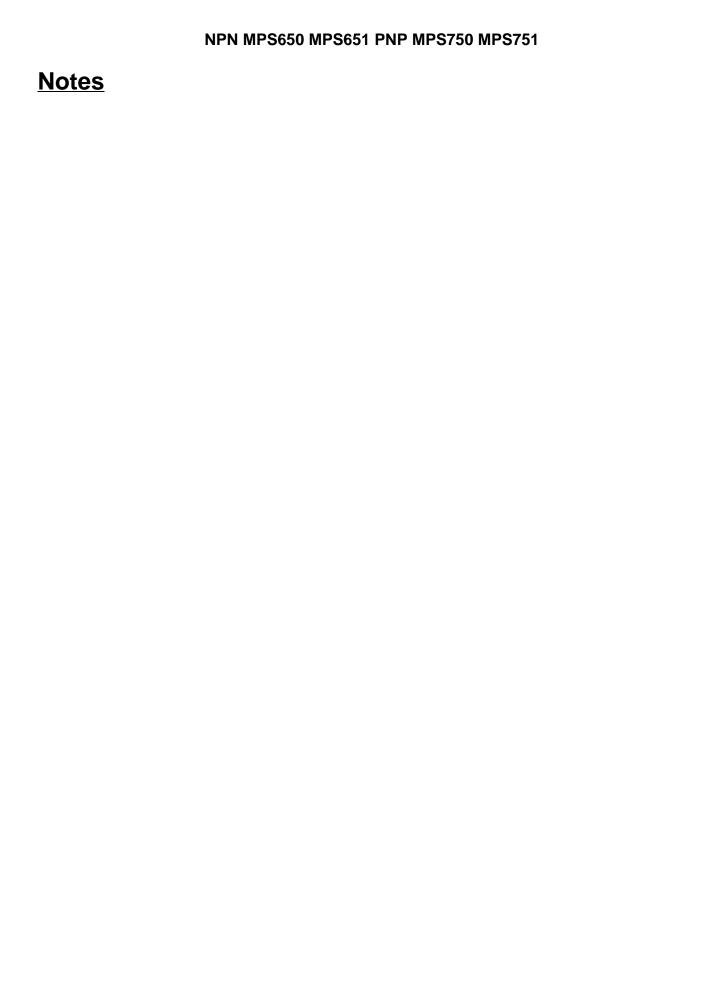
TO-92 (TO-226) CASE 29-10 **ISSUE AL** 

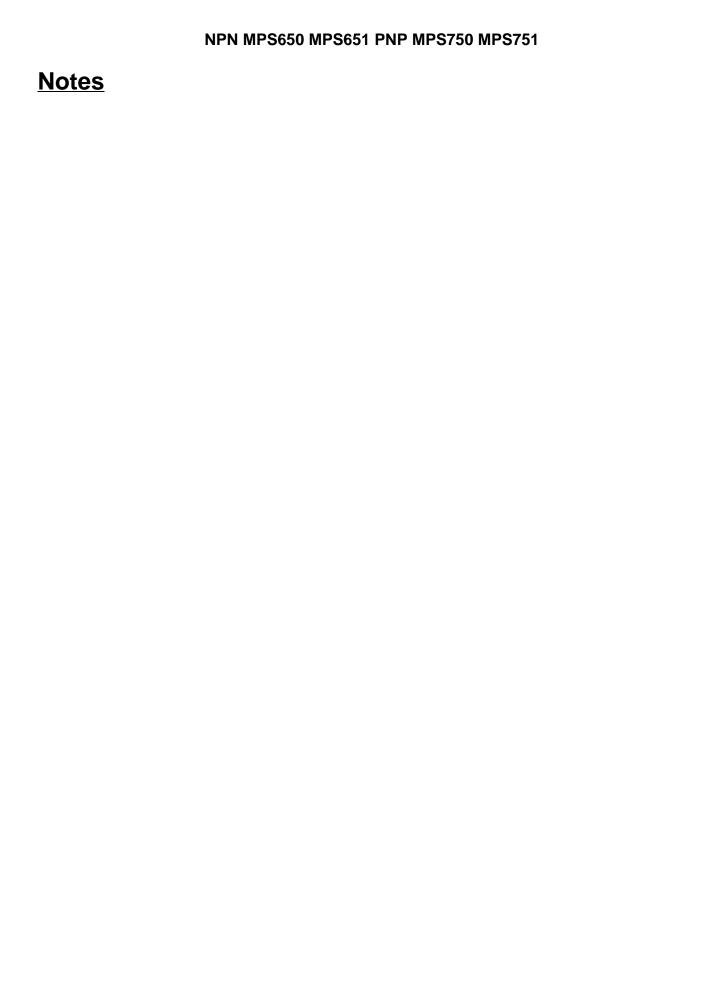


2. BASE 3. COLLECTOR

- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
  4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.021	0.457	0.533
F	0.016	0.019	0.407	0.482
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.135		3.43	





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