

August 1986 Revised March 2000

### **DM74LS05**

# **Hex Inverters with Open-Collector Outputs**

#### **General Description**

This device contains six independent gates each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

#### **Pull-Up Resistor Equations**

$$R_{MAX} = \frac{V_{CC} \left(Min\right) - V_{OH}}{N_1 \left(I_{OH}\right) + N_2 \left(I_{IH}\right)}$$

$$\mathsf{R}_{\mathsf{MIN}} = \frac{\mathsf{V}_{\mathsf{CC}} \left(\mathsf{Max}\right) - \mathsf{V}_{\mathsf{OL}}}{\mathsf{I}_{\mathsf{OL}} - \mathsf{N}_{\mathsf{3}} \left(\mathsf{I}_{\mathsf{IL}}\right)}$$

Where:  $N_1 (I_{OH}) =$ 

 $N_1$  ( $I_{OH}$ ) = total maximum output high current

for all outputs tied to pull-up resistor

 ${\rm N_2}$  (I\_{IH}) = total maximum input high current for

all inputs tied to pull-up resistor

 $N_3 \ (I_{\text{IL}}) = \text{total maximum input low current for}$ 

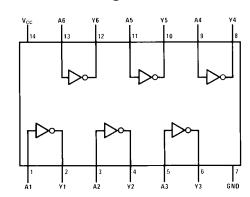
all inputs tied to pull-up resistor

#### **Ordering Code:**

Order Number	Package Number	Package Description
DM74LS05M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
DM74LS05SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
DM74LS05N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

### **Connection Diagram**



#### **Function Table**

$\mathbf{Y} = \mathbf{A}$					
Input	Output				
Α	Υ				
L	Н				
Н	L				

H = HIGH Logic Level L = LOW Logic Level

### **Absolute Maximum Ratings**(Note 1)

7V Supply Voltage Input Voltage 7V 7V Output Voltage Operating Free Air Temperature Range  $0^{\circ}\text{C}$  to  $+70^{\circ}\text{C}$ 

Storage Temperature Range  $-65^{\circ}C$  to  $+150^{\circ}C$ 

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

### **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
V <sub>OH</sub>	HIGH Level Output Voltage			5.5	V
OL	LOW Level Output Current			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

#### **Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
I <sub>CEX</sub>	HIGH Level	$V_{CC} = Min, V_O = 5.5V$			100	μА
	Output Current	V <sub>IL</sub> = Max			100	μι
V <sub>OL</sub>	LOW Level	V <sub>CC</sub> = Min, I <sub>OL</sub> = Max		0.35	0.5	V
	Output Voltage	V <sub>IH</sub> = Min				
		I <sub>OL</sub> = 4 mA, V <sub>CC</sub> = Min		0.25	0.4	
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I <sub>IL</sub>	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.36	mA
I <sub>CCH</sub>	Supply Current with Outputs HIGH	V <sub>CC</sub> = Max		1.2	2.4	mA
I <sub>CCL</sub>	Supply Current with Outputs LOW	V <sub>CC</sub> = Max		3.6	6.6	mA

Note 2: All typicals are at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

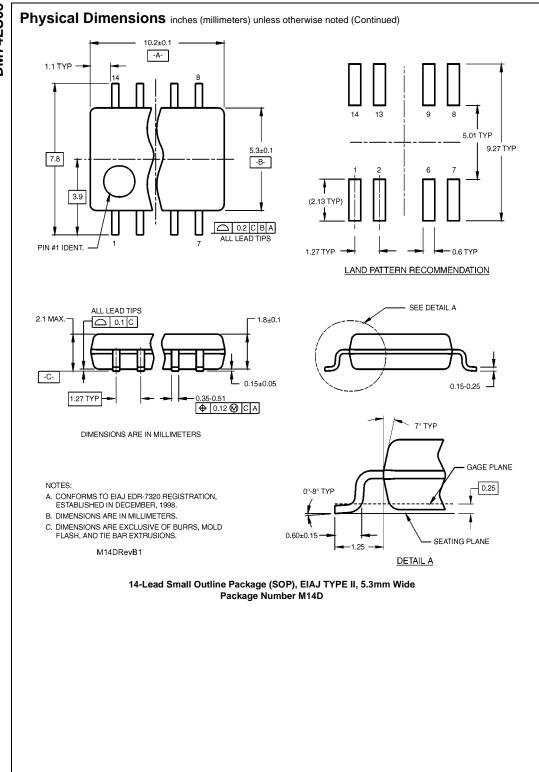
### **Switching Characteristics**

at  $V_{CC} = 5V$  and  $T_A = 25^{\circ}C$ 

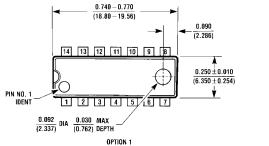
	Parameter	$R_L = 2 k\Omega$				
Symbol		C <sub>L</sub> = 15 pF		C <sub>L</sub> = 50 pF		Units
		Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Time	6	20	20	45	ns
	LOW-to-HIGH Level Output	· ·	20	20	45	113
t <sub>PHL</sub>	Propagation Delay Time	2	3 15	4	20	ns
	HIGH-to-LOW Level Output	3				

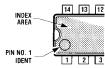
## Physical Dimensions inches (millimeters) unless otherwise noted $\frac{0.335 - 0.344}{(8.509 - 8.738)}$ $\frac{0.228 - 0.244}{(5.791 - 6.198)}$ LEAD NO. 1 IDENT $\frac{0.150 - 0.157}{(3.810 - 3.988)}$ $\frac{0.053 - 0.069}{(1.346 - 1.753)}$ $\frac{0.010 - 0.020}{(0.254 - 0.508)}$ 8° MAX TYP ALL LEADS $\frac{0.004 - 0.010}{(0.102 - 0.254)}$ SEATING PLANE 0.014 0.008 - 0.010 (0.203 - 0.254) TYP ALL LEADS $\frac{0.014 - 0.020}{(0.356 - 0.508)} \text{ TYP}$ 0.050 (1.270) TYP 0.016 - 0.050 (0.406 - 1.270) TYP ALL LEADS 0.004 (0.102) ALL LEAD TIPS 0.008 (0.203) TYP M14A (REV H)

14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow Package Number M14A

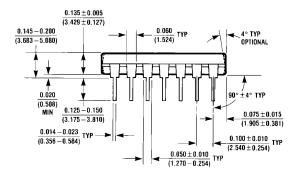


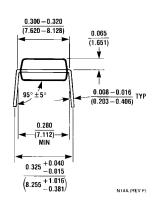
#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)





OPTION 02





14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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